



RELEASE NOTES

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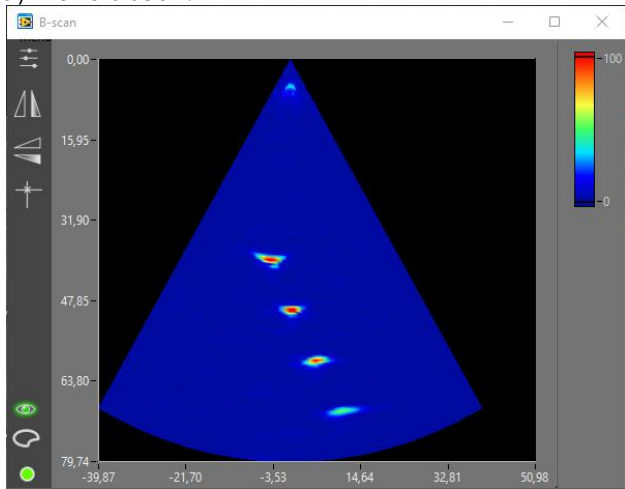
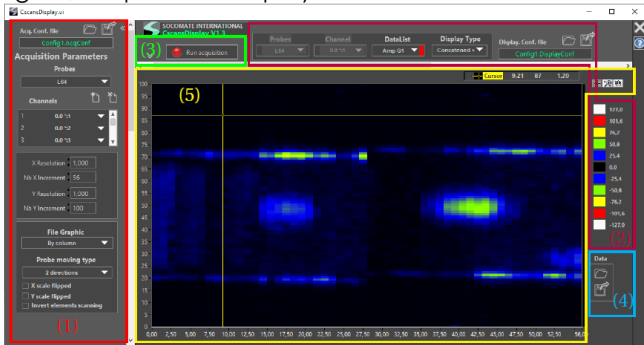
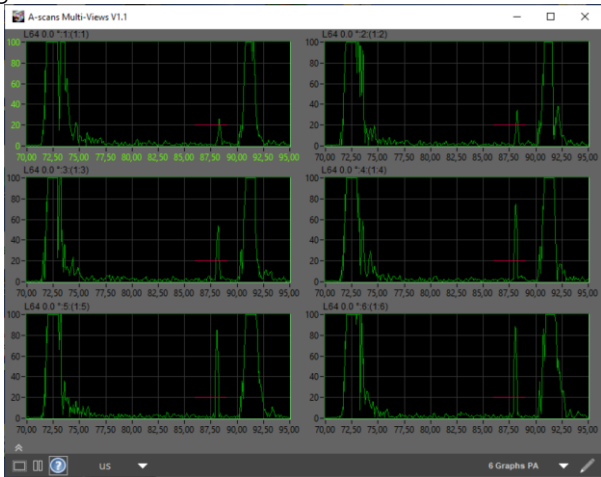
Software & firmware versions


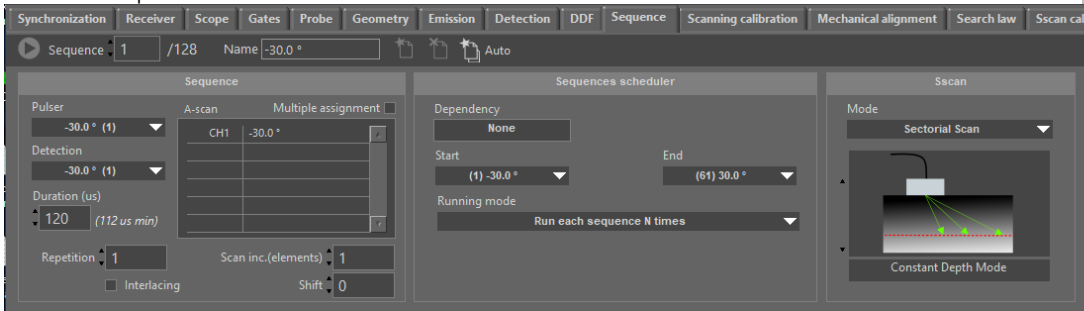
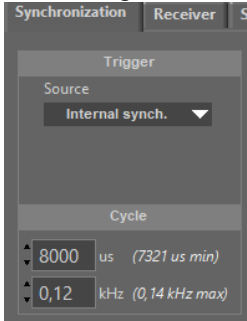
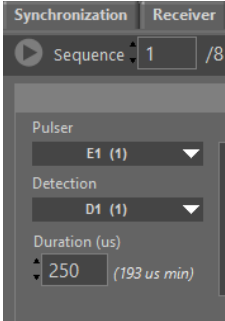
Components		Versions
API software		3.1
UTView_64.exe UTView_32.exe		
Configuration file		5.5
UTView.ini		
Language files		2.5
English.xml French.xml		
Library		8.4.1
sidev_64.dll sidev_32.dll		
Additional programs		2.5
SIPATOOLS_64.exe SIPATOOLS_32.exe		
Firmware		
Service	9.10	<u>Firmware for FFAST-PA :</u> frm_pa_250621.bin <u>Firmware for SOCO-SCAN & SOCO-SWIFT-PA :</u> frm_ssw_250621.bin <u>Firmware for SOCO-X-UT :</u> frm_soco_250621.bin <u>Configuration files for all devices :</u> cfg_generic_290621.bin <u>Additional files for FFAST-PA :</u> cfg_pa_1x32_080719.bin, cfg_pa_2x32_080719.bin, cfg_pa_3x32_080719.bin, cfg_pa_4x32_080719.bin, cfg_pa_1x64_080719.bin, cfg_pa_2x64_080719.bin, cfg_pa_1x96_080719.bin, cfg_pa_1x128_080719.bin, cfg_pa_1x32_128_080719.bin <u>Additional files for SOCO-SCAN :</u> cfg_soco_scan_1x16_250621.bin, cfg_soco_scan_1x32_250621.bin, cfg_soco_scan_plus_1x16_250621.bin, cfg_soco_scan_plus_1x32_250621.bin <u>Additional files for SOCO-SWIFT :</u> cfg_soco_swift_1x32_250621.bin, cfg_soco_swift_1x64_250621.bin, cfg_soco_swift_1x96_250621.bin, cfg_soco_swift_1x128_250621.bin, cfg_soco_swift_1x256_250621.bin, cfg_soco_swift_plus_1x32_250621.bin, cfg_soco_swift_plus_1x64_250621.bin, cfg_soco_swift_plus_1x128_250621.bin, cfg_soco_swift_plus_1x256_250621.bin, <u>Additional files for SOCO-XP-UT :</u> cfg_soco_xp_141019.bin <u>Additional files for SOCO-8S-UT :</u> cfg_soco_8s_141019.bin <u>Additional files for SOCO-1-UT & SOCO-1AB-UT:</u> cfg_soco_1_141019.bin
WebAdmin		1.3.1
SocomatePlatformManager		1.1.3
Interface modules		
INTLOG_IN:	3.0	INTLOG_IN_V_3_0_0.pof
INTANA_OUT:	2.3.1	INTANA_OUT_V_2_3_1.pof (model < c)
	3.1.4	INTANA_OUT_V_3_1_4.pof (model ≥ c)
INTLOG_OUT:	2.3.1	INTLOG_OUT_V_2_3_1.pof (model < b)
	3.1.3	INTLOG_OUT_V_3_1_3.pof (model ≥ b)




Where is indicated model

New/Improved features

ID	Description
	<p>Some programs and tools have been added:</p> <p>Bscan_xx (for SOCO-SCAN, SOCO-SWIFT and FAAS-PA products) A program to display B and S-scan.</p>  <p>CscansDisplay_xx(for all devices) A program to configure, acquire and display C-scan data.</p>  <p>AscansMultiView_xx (for all devices) A program to configure and show several A-scans on a window.</p>  <p>UT_dependencies_xx A tool to check and ZIP an UT file with all this dependencies files such as laws, probe, ...</p> <p>VFAnalyzer_xx A program to check the file versions of the installed components.</p> <p>Note: These programs are named with the number of bit of the target Ex: B-scan_32.exe or B-scan_64.exe</p>

	<p>A new COMPRESSED_AS_USPC A-scan mode has been added. This mode is reserved for SOCO-X products at FS=200MHz. This mode has been designed to return the A-scan signal as the old USPC products done.</p>
	<p>A new S-scan mode has been added. The tab sequence has been modified to manage this mode. New tabs have been added to calibrate the S-scan and to set a DAC You can call Bscan_xx program to get a graphic representation of S-scan during the calibration process.</p>  <p>NOTE: This mode works with new laws calculated with SIPATOOLS 2.5</p>
	<p>Now, the time performances are displayed to know the maximum achievable performance according to the current settings.</p>  
	<p>New alarm process to check temperatures and voltage has been embedded. This process periodically checks these parameters to set LED status.</p>
	<p>Improvement of plugins Displaying a message box to apply changes if certain changes were detected when the operator exits the current tab.</p>
	<p>Improvement of IO cards performances</p> <ul style="list-style-type: none"> • Improve boot process (auto-check) • Increase the cable length up to 50m • Manage a LED to indicate the status of the cards • Enable IO card with SOCO-SCAN and SOCO-SWIFT • Correct a bug of the output of TOF (was 5V when TOF = 0usec) • Reset the output to 0V when an IO pin is unselected.
	<p>New parameters are available with the DLL:</p> <ul style="list-style-type: none"> • "sync_period_min", "sequence_time_min" to get time performance • "path" to get X, Y coordinate of points of the A-scan signal (*). • "gate_path" to get X, Y coordinate of points of the gates (*). • "gateX_projection", "gateX_relative_projection" to get the X, Y coordinate of a distance measured into a gate (*). • "gatex_inversed_projection", "gatex_relative_inversed_projection" to get an ultrasonic distance from an X, Y coordinate (*). <p>(*) Request a "law" created by the new SIPATOOL program. These parameters are used for B and S-scan representation.</p>
	<p>Improvement of Scanning-tool to calibrate the multi-beams configuration in one shot.</p>

	<p>The new DLL checks the INI configuration file to detect incompatibilities between selected options.</p> <p>Example: COMPRESSED_AS_USPC is not available for FFAST-PA, SOCO-SCAN and SOCO-SWIFT.</p> <p>An error will be detected and you could find details into the log file.</p>
 <p>Behaviour has been changed</p>	<p>The frequency sampling at 66.6MHz has been removed.</p>

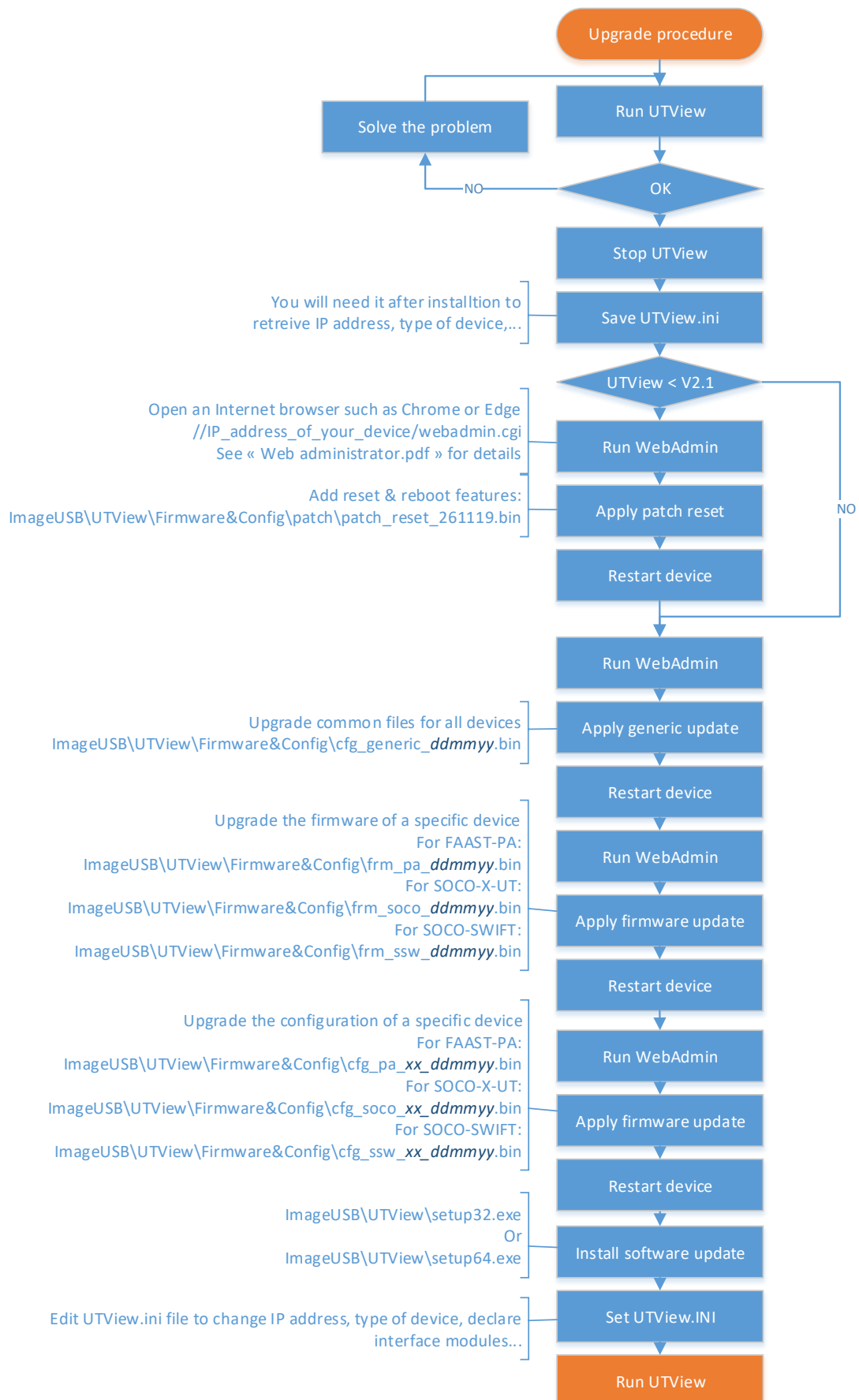
Bugs fixed

ID	Description
	The firmware has been updated:
	<ul style="list-style-type: none"> To return all data on finite number of triggers.
	<ul style="list-style-type: none"> To improve a dead zone at the beginning of the A-scan.
	<ul style="list-style-type: none"> To run A-scans with COMPRESSED and EXTENDED mode at 8/9 bits for all frequency sampling.
	<ul style="list-style-type: none"> To improve the element calibration process by eliminating inactive elements
	<ul style="list-style-type: none"> To improve the Y calibration of the A-scan signal.
	<ul style="list-style-type: none"> To improve the loading of the firmware when we change the A-scan mode.
	<ul style="list-style-type: none"> To force divider to 1 when internal trigger is selected.
	<ul style="list-style-type: none"> To correct the management of LED READY. It was OFF when a UT file designed for element calibration was loaded.
	<ul style="list-style-type: none"> To correct interlacing mode. <ul style="list-style-type: none"> Sends interlacing value to file upload Auto recalculate the interlacing value if the repetition number changes

Bug fixed and improvements by patch 1

ID	Description
#388	Correct a bug when gates use inches
#370	Add missing source codes for LabView developers.
#334	Add signal description of K2 connector of SOCO-X cards.
#335	Correct the Developer's manual. "device%d_hv" command description wasn't correct.
#312	The 32 bit Detection plugin (for Phased Array devices) wasn't up to date.
#387	The firmware of the Interface output cards have been improved.
#400	
#405	
#379	Update VFAnalyzer program (<i>a tool to check version files</i>)

How to upgrade an existing system



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