

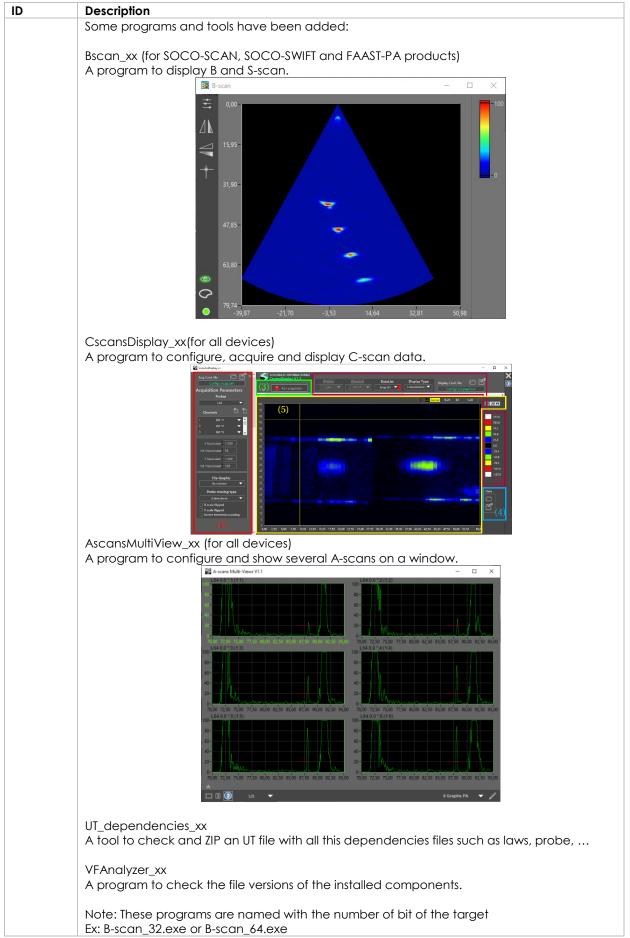
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Software & firmware versions

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cfg_pa_1x64_080719.bin, cfg_pa_2x64_080719.bin, cfg_pa_1x96_080719.bin, cfg_pa_1x128_080719.bin, cfg_pa_1x32_128_080719.bin <u>Additional files for SOCO-SCAN :</u> cfg_soco_scan_1x16_250621.bin, cfg_soco_scan_1x32_250621.bin, cfg_soco_scan_plus_1x16_250621.bin, cfg_soco_scan_plus_1x32_250621.bin <u>Additional files for SOCO-SWIFT :</u> cfg_soco_swift_1x32_250621.bin, cfg_soco_swift_1x64_250621.bin, cfg_soco_swift_1x96_250621.bin,	
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cfg_soco_swift_1x96_250621.bin,	
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cfg_soco_swift_1x256_250621.bin,	
cfg_soco_swift_plus_1x32_250621.bin,	
cfg_soco_swift_plus_1x64_250621.bin,	
cfg_soco_swift_plus_1x128_250621.bin,	
cfg_soco_swift_plus_1x256_250621.bin,	
Additional files for SOCO-XP-UT :	
cfg_soco_xp_141019.bin	
Additional files for SOCO-8S-UT :	
cfg_soco_8s_141019.bin	
Additional files for SOCO-1-UT & SOCO-1AB-UT:	
cfg_soco_1_141019.bin	
WebAdmin 1.3.1	
SocomatePlatformManager 1.1.3	
Interface modules	
INTLOG_IN: 3.0 INTLOG_IN_V_3_0_0.pof	
	0 0 0
INTANA_OUT: 2.3.1 INTANA_OUT_V_2_3_1.pof (model < c)	0 9 9
3.0.3 INTANA_OUT_V_3_0_3.pof (model≥c)	
INTLOG_OUT: 2.3.1 INTLOG_OUT_V_2_3_1.pof (model < b) 3.0.2 INTLOG_OUT_V_3_0_2 pof (model < b) Where is indicated	
3.0.2 INTLOG_OUT_V_3_0_2.pof (model≥b) Where is indicated	

New/Improved features



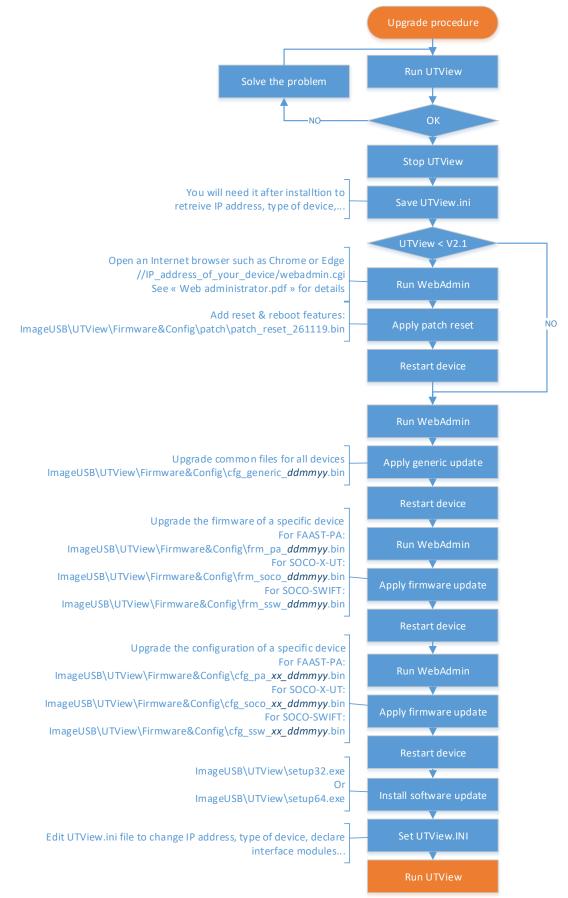
	A new COMPRESSED_AS_USPC A-scan mode has been added. This mode is reserved for SOCO-X products at FS=200MHz.
	This mode has been designed to return the A-scan signal as the old USPC products done. A new S-scan mode has been added. The tab sequence has been modified to manage this mode. New tabs have been added to calibrate the S-scan and to set a DAC You can call Bscan_xx program to get a graphic representation of S-scan during the calibration process. Synchronization Receiver Scope Gates Probe Geometry Emission Detection DDF Sequence Scanning calibration Mechanical alignment Search law Sscan cali Sequence 1 /128 Name 30.0* Pulser Multiple assignment Dependency Start (1) -30.0* Pulser (61) 30.0* (61) 30.0* (120 / 112 us min) (120 / 112 us min) (12
A	Repetition Image: Constant Depth Mode Interfacing Shift NOTE: This mode works with new laws calculated with SIPATOOLS 2.5
	Now, the time performances are displayed to know the maximum achievable performance according to the current settings.
	New alarm process to check temperatures and voltage has been embedded. This process periodically checks these parameters to set LED status.
	Improvement of plugins Displaying a message box to apply changes if certain changes were detected when the operator exits the current tab.
	 Improvement of IO cards performances Improve boot process (auto-check) Increase the cable length up to 50m Manage a LED to indicate the status of the cards Enable IO card with SOCO-SCAN and SOCO-SWIFT Correct a bug of the output of TOF (was 5V when TOF = 0usec) Reset the output to 0V when an IO pin is unselected.
	 New parameters are available with the DLL: "sync_period_min", "sequence_time_min" to get time performance "path" to get X, Y coordinate of points of the A-scan signal (*). "gate_path" to get X, Y coordinate of points of the gates (*). "gateX_projection", "gateX_relative_projection" to get the X, Y coordinate of a distance measured into a gate (*). "gatex_inversed_projection", "gatex_relative_inversed_projection" to get an ultrasonic distance from an X, Y coordinate (*). (*) Request a "law" created by the new SIPATOOL program. These parameters are used for
	B and S-scan representation. Improvement of Scanning-tool to calibrate the multi-beams configuration in one shot.

	The new DLL checks the INI configuration file to detect incompatibilities between selected options.
	Example: COMPRESSED_AS_USPC is not available for FAAST-PA, SOCO-SCAN and SOCO- SWIFT.
	An error will be detected and you could find details into the log file.
	The frequency sampling at 66.6MHz has been removed.
Behaviour	
has been	
changed	

Bug fixed

ID	Description
	The firmware has been updated:
	 To return all data on finite number of triggers.
	 To improve a dead zone at the beginning of the A-scan.
	 To run A-scans with COMPRESSED and EXTENDED mode at 8/9 bits for all frequency sampling.
	To improve the element calibration process by eliminating inactive elements
	To improve the Y calibration of the A-scan signal.
	 To improve the loading of the firmware when we change the A-scan mode.
	 To force divider to 1 when internal trigger is selected.
	 To correct the management of LED READY. It was OFF when a UT file designed for element calibration was loaded.
	 To correct interlacing mode. Sends interlacing value to file upload Auto recalculate the interlacing value if the repetition number changes

How to upgrade an existing system



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