

RELEASE NOTES

Components	Versions	
API software	2.2	
UTView_64.exe		
UTView_32.exe		
Configuration file		
UTView.ini	5.2	
Language files	2.0	
English.xml		
French.xml		
Library	7.4	
sidev_64.dll		
sidev_32.dll	0.0.5	
Additional programs	2.0.5	
SIPATOOLS_64.exe		
SIPATOOLS_32.exe		
Firmware files FAAST-PA		frag. 1000000 him
FAASI-PA		frm_pa_120320.bin
SOCO-SWIFT-PA		frm ssw 100320.bin
3000-3WII I-I A		1111_\$\$W_100320.DII1
SOCO-X-UT		frm_soco_100320.bin
3000 X 01		1111_30C0_100020.DII1
Configuration files		
For all devices		cfg_generic_120320.bin
		5-5
Additional files		
FAAST-PA		cfg_pa_1x32_080719.bin, cfg_pa_2x32_080719.bin,
		cfg_pa_3x32_080719.bin, cfg_pa_4x32_080719.bin,
		cfg_pa_1x64_080719.bin, cfg_pa_2x64_080719.bin,
		cfg_pa_1x96_080719.bin, cfg_pa_1x128_080719.bin,
		cfg_pa_1x32_128_080719.bin
0000 CANAL DA		of a save 1/20 100200 bits of a save 1/4 100200 bits
SOCO-SWIFT-PA		cfg_ssw_1x32_100320.bin, cfg_ssw_1x64_100320.bin,
		cfg_ssw_1x96_100320.bin, cfg_ssw_1x128_100320.bin
SOCO-XP-UT		cfg_soco_xp_141019.bin
3000-71-01		CI9_30CO_AP_141017.DII1
SOCO-8S-UT		cfg_soco_8s_141019.bin
		5.8_5555_55_1 11017.bii1
SOCO-1-UT		cfg_soco_1_141019.bin
SOCO-1 AB-UT		<u> </u>
Interface modules		
INTLOG_IN:	3.0	INTLOG_IN_V_3_0_0.pof
INTANA_OUT:	2.3.1	INTANA_OUT_V_2_3_1.pof
INTLOG_OUT:	2.3.1	INTLOG_OUT_V_2_3_1.pof

New/Improved features

ID	Description
	Added a switchable attenuation cell (0, -6, -12 and -18 dB) to the SOCO-X product
	receiver.
	This feature is very useful when a strong signal is returned from the probe.
	Now, the 6dB gain booster is applied to all filters when activated in the configuration file
	(UTView.ini).
	The behavior of "Device Ready" available on INTLOG_IN module on pin 37 has been
	changed.
	Now, when the output transistor is blocked, it means that "the device is not ready" and
	when it is passing, it means that "the device is ready".
	The behavior of "Device Ready" on pin 42 has not been changed to preserve the
	compatibility with older systems.
	Read "LOG INPUT Manual.pdf" to get more details.
	New syncs have been added to monitor the flow of a device's sequences. It is now
	possible to control a device, via the LINK bus, with the inputs to the card INTLOG_IN
	connected to another device.
	For instance, you can synchronize several devices like this:
	DEVICE 1 DEVICE 2 DEVICE 3 LINK IN LINK OUT LINK IN LINK OUT Z6 External trigger on input Z6
	External trigger on input A5
	Read "LOG INPUT Manual.pdf" to get more details.
	UTView and the firmware haves been upgraded to manage more one output port. Now,
	the four output ports are available on back panel of the FAAST-PA.
	SIPATOOLS and its help file have been upgraded to add the annular probe into the list of
	multi-elements probes.
	The firmware has been upgraded to drive SOCO-SWIFT-PA with 128 channels.

Bug fixed

ID	Description

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