






RELEASE NOTES

Components	Versions
API software UTView_64.exe UTView_32.exe	2.1
Library Sidev_64.dll Sidev_32.dll	7.0
Additional programs SIPATOOLS_64.exe SIPATOOLS_32.exe	2.0
Firmware FAAST-PA SOCO-SWIFT-PA SOCO-X-UT	frm_pa_131219.bin frm_ssw_131219.bin frm_soco_131219.bin
Configuration files For all devices Additional files FAAST-PA	cfg_generic_131219.bin cfg_pa_1x32_080719.bin, cfg_pa_2x32_080719.bin, cfg_pa_3x32_080719.bin, cfg_pa_4x32_080719.bin, cfg_pa_1x64_080719.bin, cfg_pa_2x64_080719.bin, cfg_pa_1x96_080719.bin, cfg_pa_1x128_080719.bin, cfg_pa_1x32_128_080719.bin
SOCO-XP-UT	cfg_soco_xp_141019.bin
SOCO-8S-UT	cfg_soco_8s_141019.bin
SOCO-1-UT SOCO-1AB-UT	cfg_soco_1_141019.bin
Interface modules INTLOG_IN: INTANA_OUT: INTLOG_OUT:	INTLOG_IN_V_2_3.pof INTANA_OUT_V_2_3_1.pof INTLOG_OUT_V_2_3_1.pof

New/Improved features

ID	Description
	<p>Two new devices manageable with UTVlew .</p> <p><u>SOCO-1AB-UT</u> It is a single probe board enabling to process this probe in two classes of controls.</p> <p>The device offers two parallels channels working on the same probe.</p> <p><u>SOCOSWIFT-PA</u> It is a full parallel Phased Array device with square waves pulses.</p>
	<p>Two new programs to reset or restart devices.</p> <p>RESET_Devices is a light restart (reboot app on devices)</p> <p>REBOOT_Devices is a strong restart as it occurs when the devices are activated.</p>
	C++ and C# samples have been added to the SDK
	<p>New calibration tool for Phased Array devices</p> <p>This tool calibrates the Time of Flight for each element.</p>
	Taking into account the aperture of laws to determine scanning limits.
	<p>A new feature to check the coherence between the scope and gates.</p> <p>A pop message appears when scope and gates don't match.</p> <div data-bbox="518 857 1195 1283" data-label="Image"> </div> <p>Set WAVE_MATCHING parameter into UTVlew.ini file to enable or disable this feature.</p>
	<p>The sensitivity of the filters of SOCO-X products has been aligned to obtain a more harmonious response.</p> <p> If you update your software this may require the adjustment of gains from your old files.</p>
	<p>The receiver gain of SOCO-X-UT products has been increased about 19dB when FS 100Mhz is selected.</p> <p>It was done to get a similar result such as the USPC product.</p> <p> If you update your software this may require the adjustment of gains from your old files.</p>
	<p>Adding two modules on receiver to shift the gain dynamics for SOCO-X products. These modules can be enabled/disabled from the configuration file (UTVlew.ini).</p> <p>SHIFT_6dB: Adding 6 dB on gain dynamics for 7.5-15 MHz filter</p> <p>SHIFT_10dB: Adding 10 dB on gain dynamics for 1-1.5MHz filter</p>

	<p>New probe range for Phased Array devices</p> <p>We reordered the probe range lists to get a more intuitive experience.</p> <p> Be careful if you update an existing system you will need to change your settings files to select the filter suitable for your application!</p>
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Bug fixed

ID	Description
	A correction has been done to improve the boot of FFAST-PA
	The firmware has been updated to properly manage the FFAST-PA 1x96
	A correction has been done to control the high voltage of SOCO-8S-UT and SOCO-XP-UT devices.
	A correction has been done on TOF and WT analog outputs when Gate IF is disabled and when the range is less than 640ns.
	Fixed cycle counter increment of cycle and acquisition in Cscan frames that were incremented on the index of the last scan and not the first.
	Removing the cycle alarm when changing the cycle duration.

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