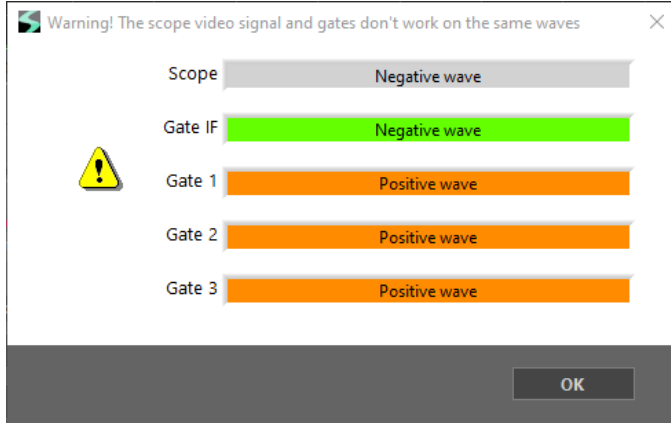


RELEASE NOTES

Components	Versions
API software UTView_64.exe UTView_32.exe	2.1
Library Sidev_64.dll Sidev_32.dll	7.0
Additional programs SIPATOOLS_64.exe SIPATOOLS_32.exe	2.0
Firmware FAAST-PA SOCO-X-UT	frm_pa_080719.bin frm_soco_141019.bin
Configuration files For all devices Additional files FAAST-PA	cfg_generic_141019.bin cfg_pa_1x32_080719.bin, cfg_pa_2x32_080719.bin, cfg_pa_3x32_080719.bin, cfg_pa_4x32_080719.bin, cfg_pa_1x64_080719.bin, cfg_pa_2x64_080719.bin, cfg_pa_1x96_080719.bin, cfg_pa_1x128_080719.bin, cfg_pa_1x32_128_080719.bin
SOCO-XP-UT	cfg_soco_xp_141019.bin
SOCO-8S-UT	cfg_soco_8s_141019.bin
SOCO-1-UT SOCO-1AB-UT	cfg_soco_1_141019.bin
Interface modules INTLOG_IN: INTANA_OUT: INTLOG_OUT:	INTLOG_IN_V_2_3.pof INTANA_OUT_V_2_3_1.pof INTLOG_OUT_V_2_3_1.pof

New/Improved features

ID	Description
	<p>Add a new device SOCO-1AB-UT. SOCO-1AB-UT is a single probe board enabling to process this probe in two classes of controls. The device offers two parallels channels working on the same probe.</p>
	<p>A new feature to check the coherence between the scope and gates. A pop message appears when scope and gates don't match.</p>  <p>Set WAVE_MATCHING parameter into UTVView.ini file to enable or disable this feature.</p> <pre> 14 15 WAVE_MATCHING=ON/OFF 16 17 18 19 [Global] 20 VIDEO_REJECTION=REJECT 21 WAVE_MATCHING=ON 22 [/Global] 23 </pre> <p>REJECT mode if undefined. This setting verifies (when ON) the coherence between the representation of the A-scan signal and the phases of the gates, and then displays a pop up message if there is no match. OFF if undefined.</p>
	<p>The sensitivity of the filters of SOCO-X products has been aligned to obtain a more harmonious response.</p> <p>⚠ If you update your software this may require the adjustment of gains from your old files.</p>
	<p>The receiver gain of SOCO-X-UT products has been increased about 19dB when FS 100Mhz is selected. It was done to get a similar result such as the USPC product.</p>
	<p>A C# sample has been added to the SDK</p>

Bug fixed

ID	Description
	A correction has been done to improve the boot of FFAST-PA
	The firmware has been updated to properly manage the FFAST-PA 1x96
	A correction has been done to control the high voltage of SOCO-8S-UT and SOCO-XP-UT devices.
	A correction has been done on TOF and WT analog outputs when Gate IF is disabled and when the range is less than 640ns.

BETA

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